

10/22/2016 16:23:59

System And Method For Real Time Monitoring And  
Control Of Networked Computers

Inventor: Robert F. Terry

Serial No.: 09/827,891

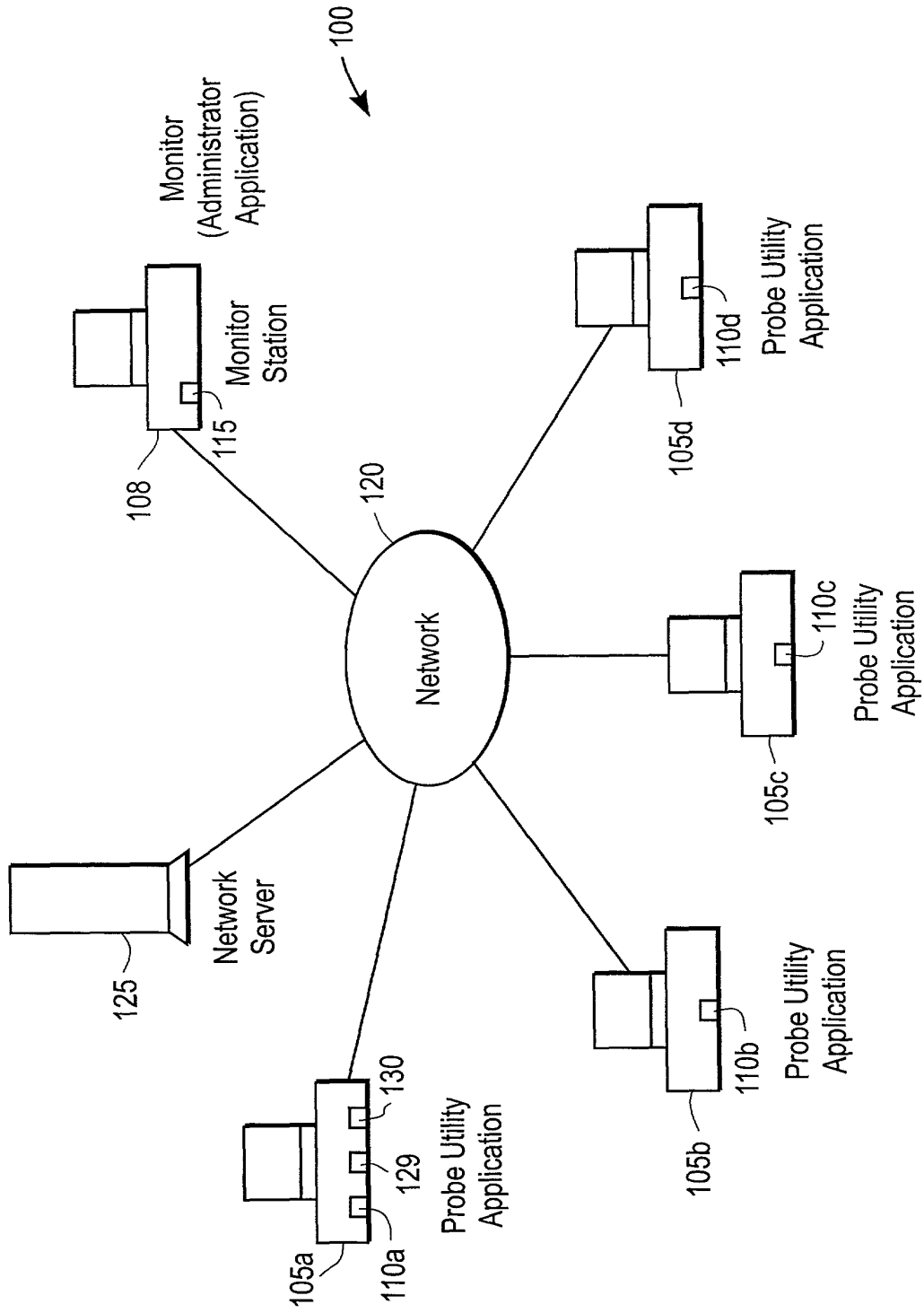


FIG. 1

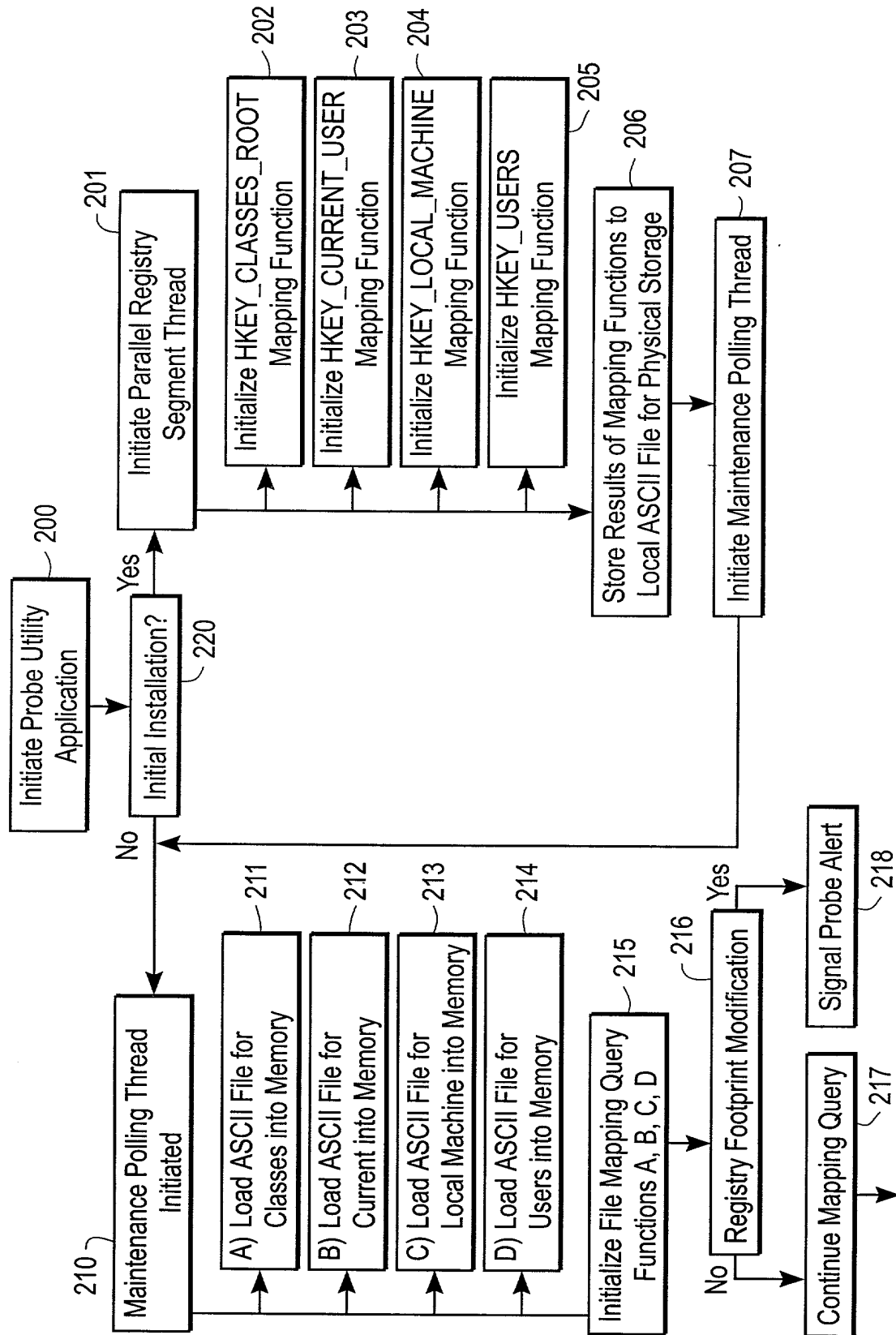


FIG. 2



FIG. 3

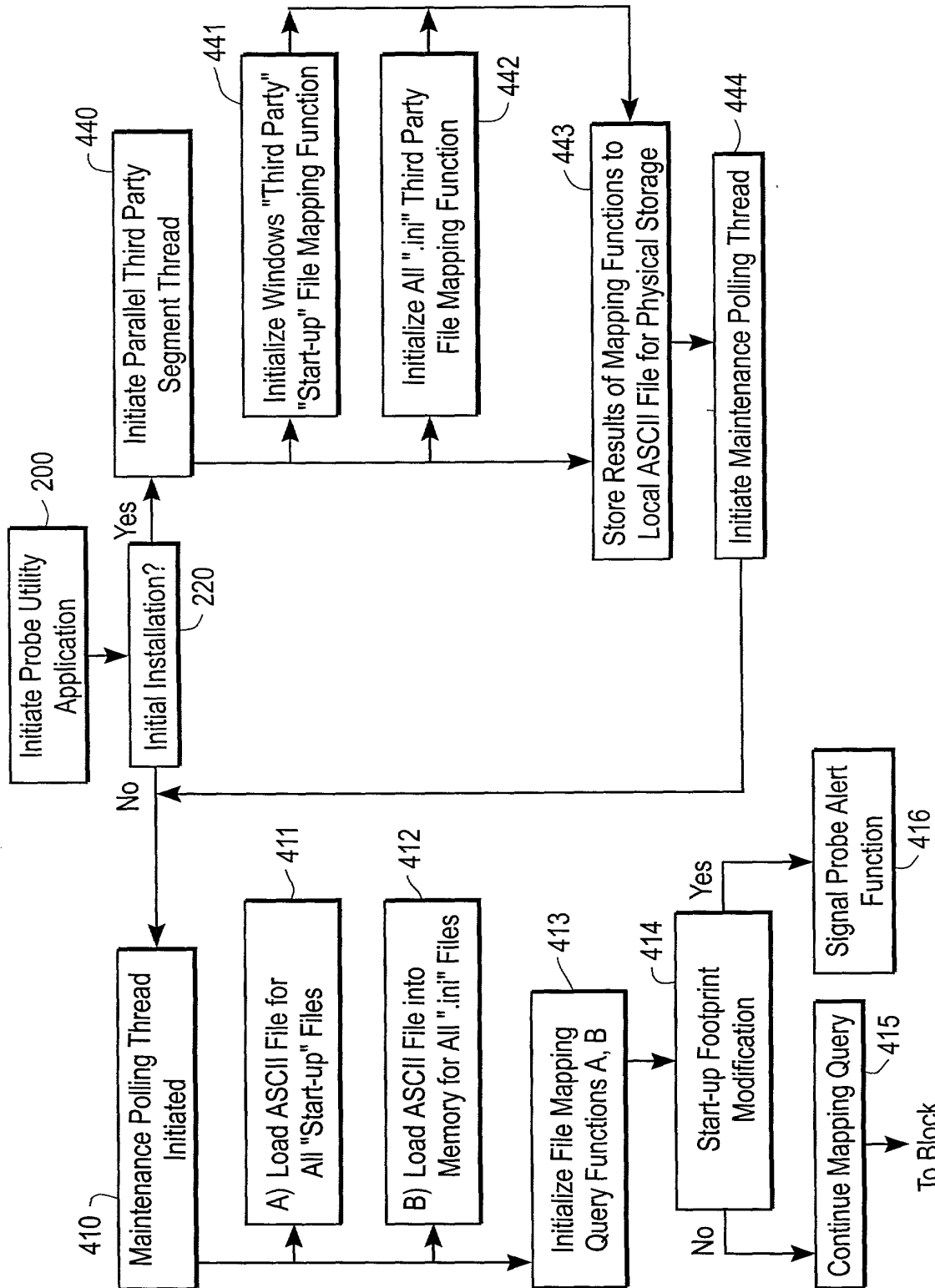


FIG. 4

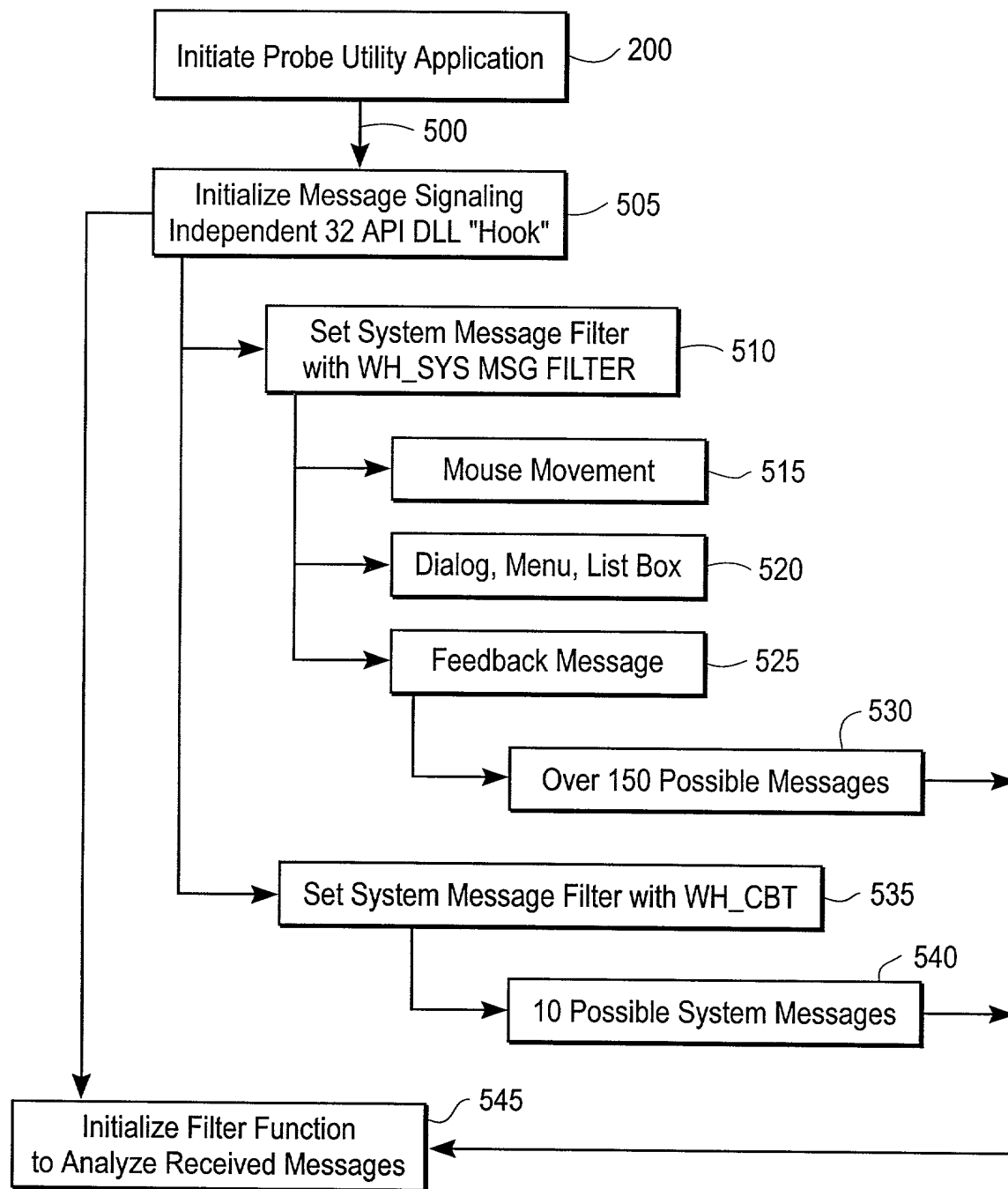


FIG. 5

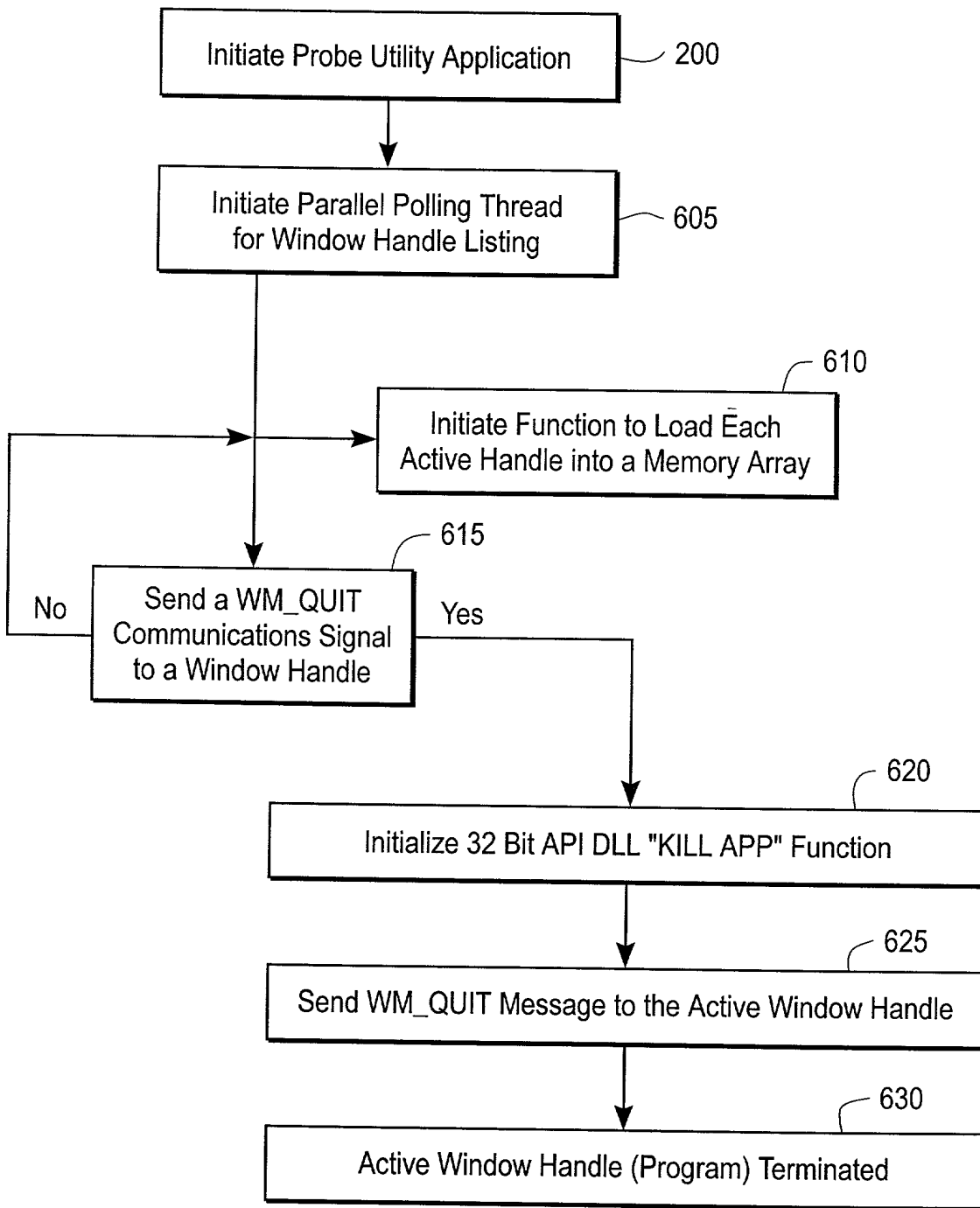


FIG. 6

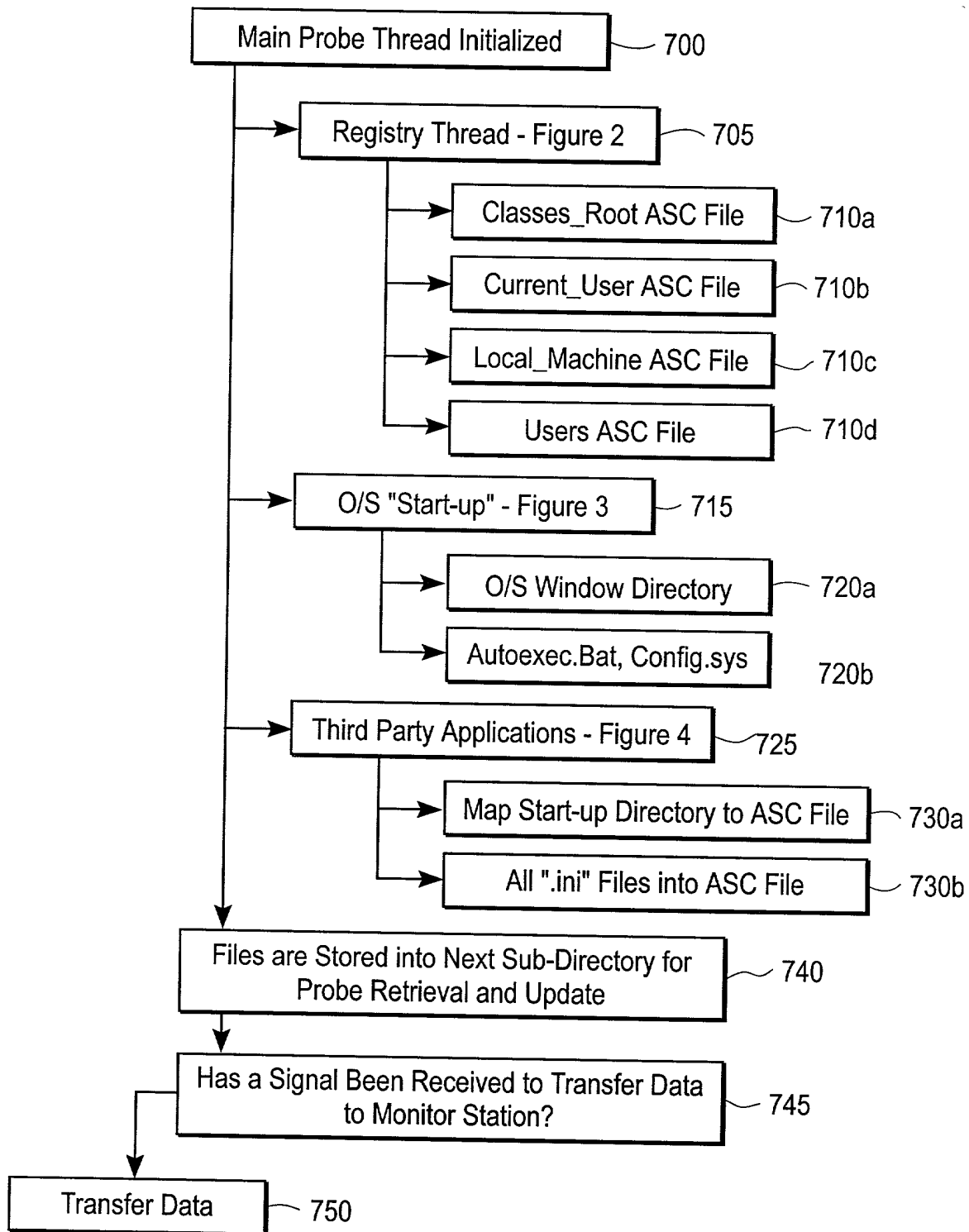


FIG. 7

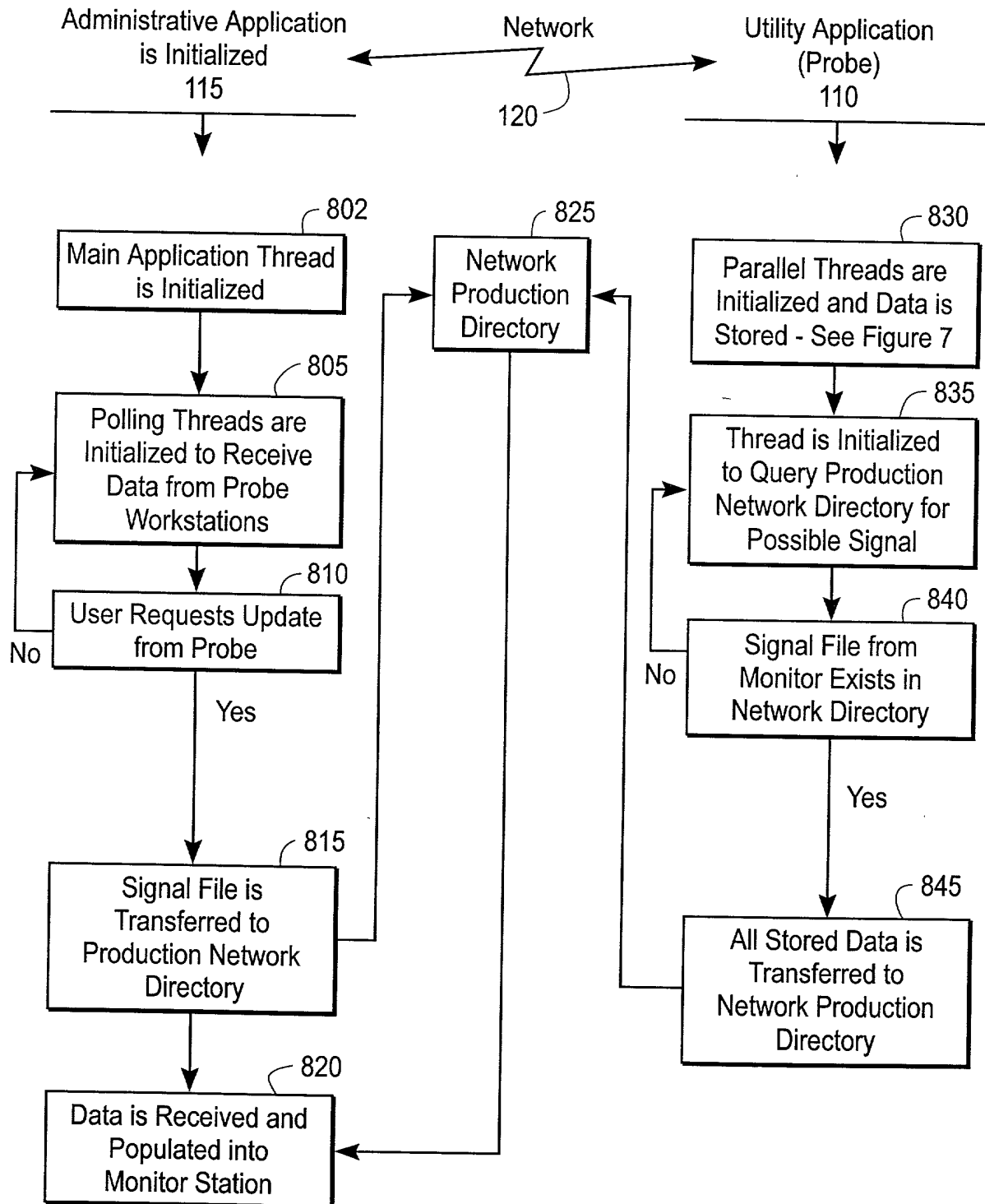


FIG. 8



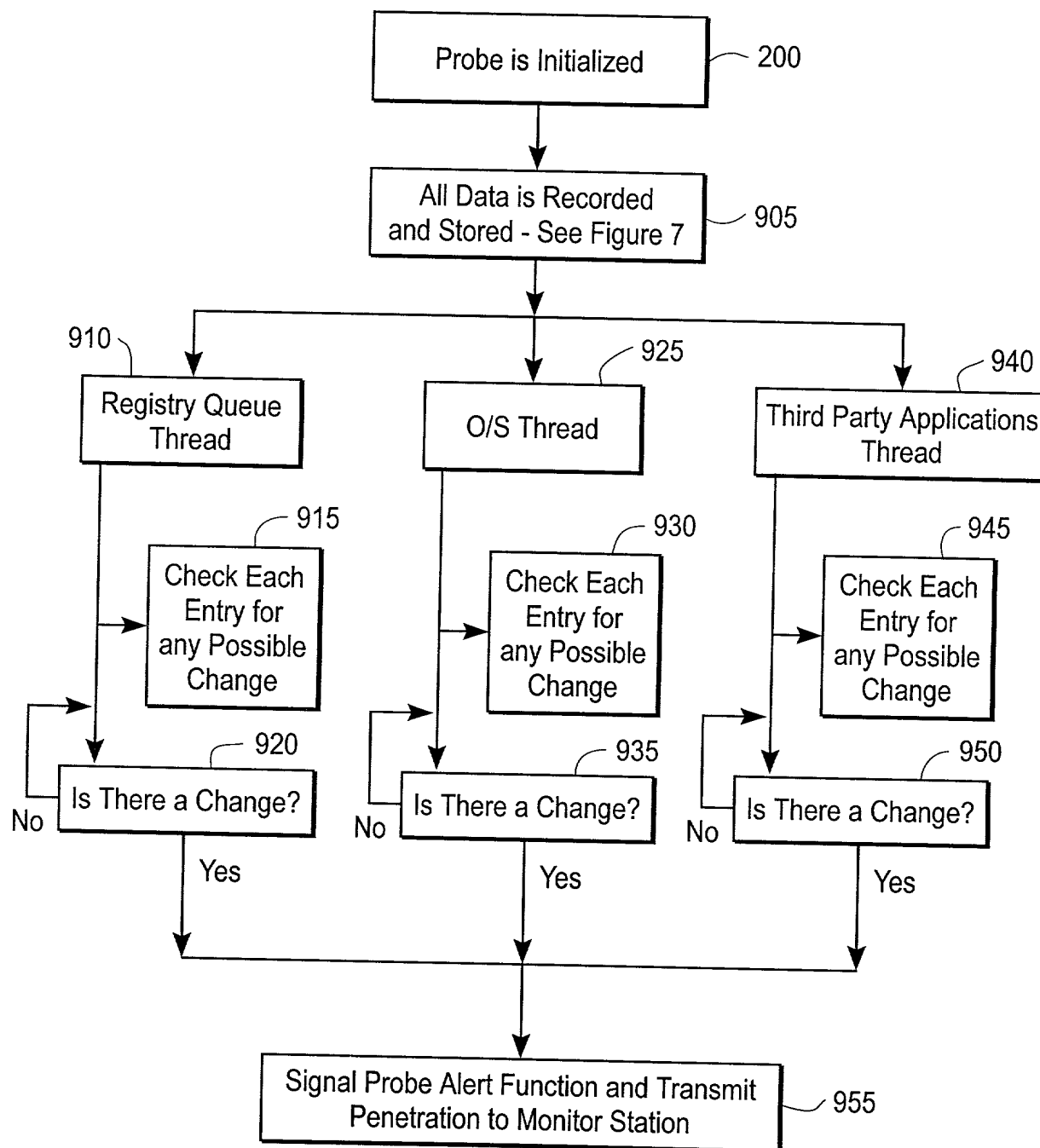


FIG. 9

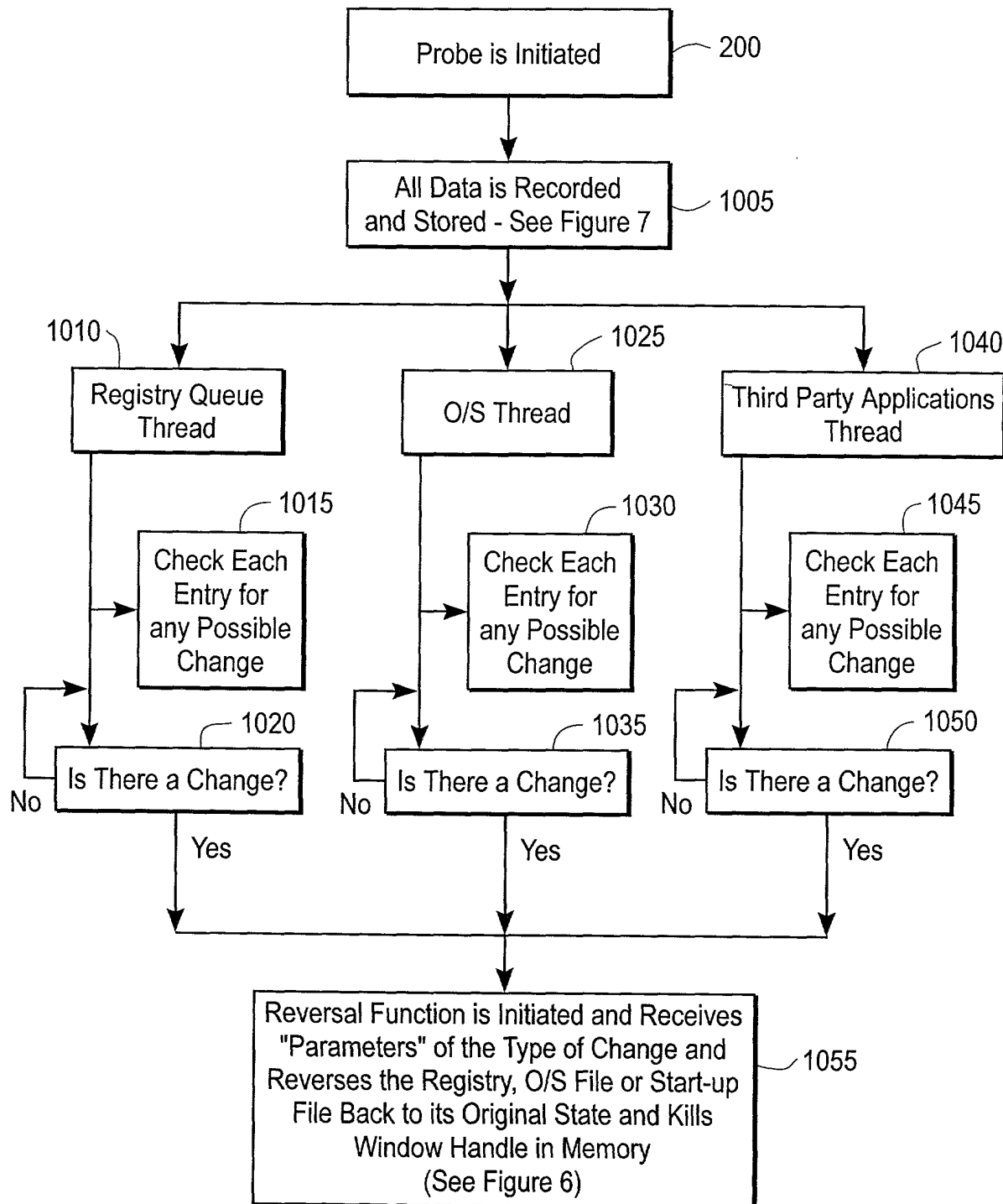


FIG. 10

*System And Method For Real Time Monitoring And  
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1100

SOT [CR] [LF]

Date=CCYY\MM\DD [cr] [lf]

Time=HH : MM : SS [cr] [lf]

3Wind=Variable Up To 500 Characters [cr] [lf]

2Wind=Variable Up To 500 Characters [cr] [lf]

1Wind=Variable Up To 500 Characters [cr] [lf]

Mssg=Variable Up To 500 Characters [cr] [lf]

EOT [cr] [lf]

Structured Signal File Block Diagram

FIG. 11

09/827,891, 07/27/01

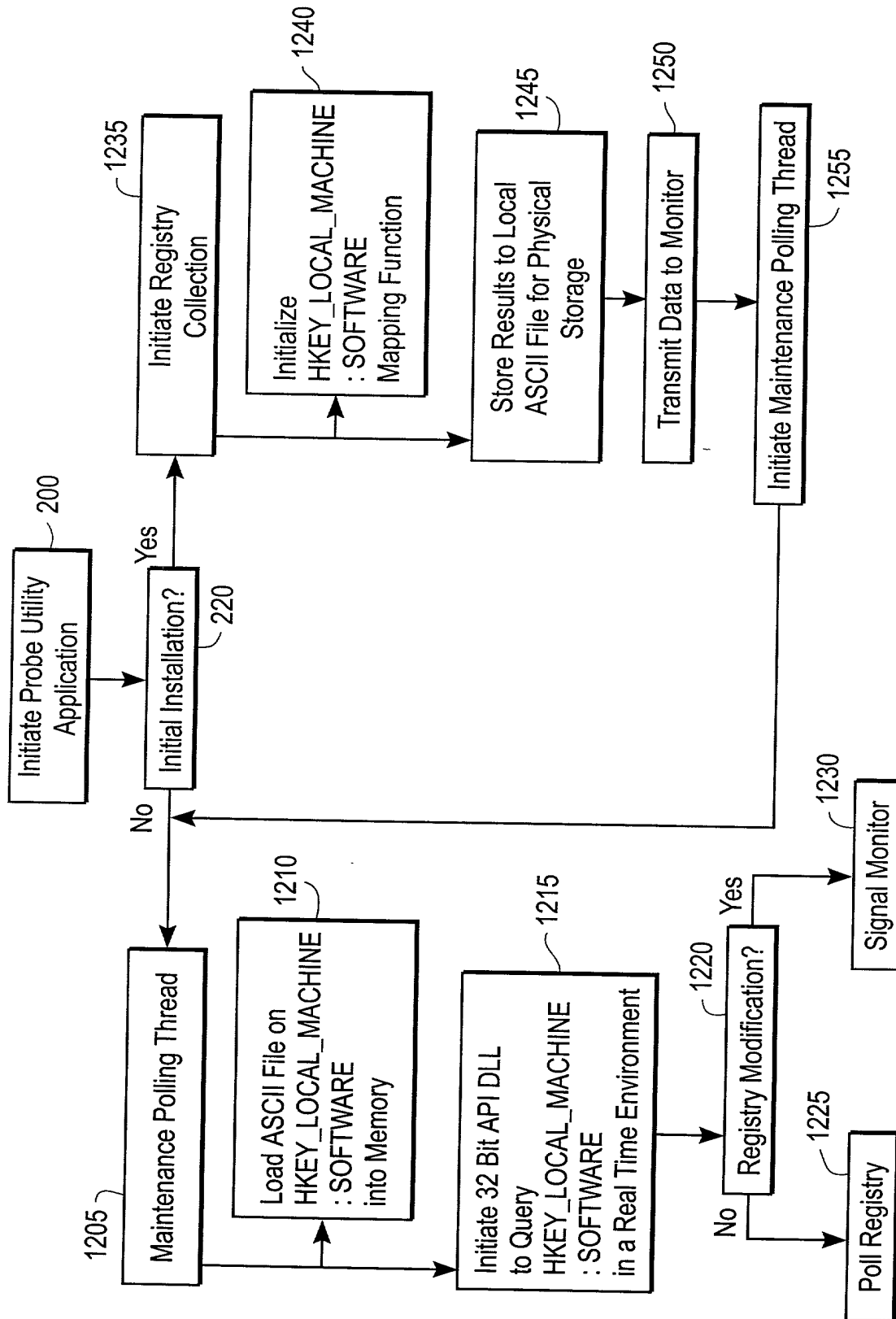


FIG. 12

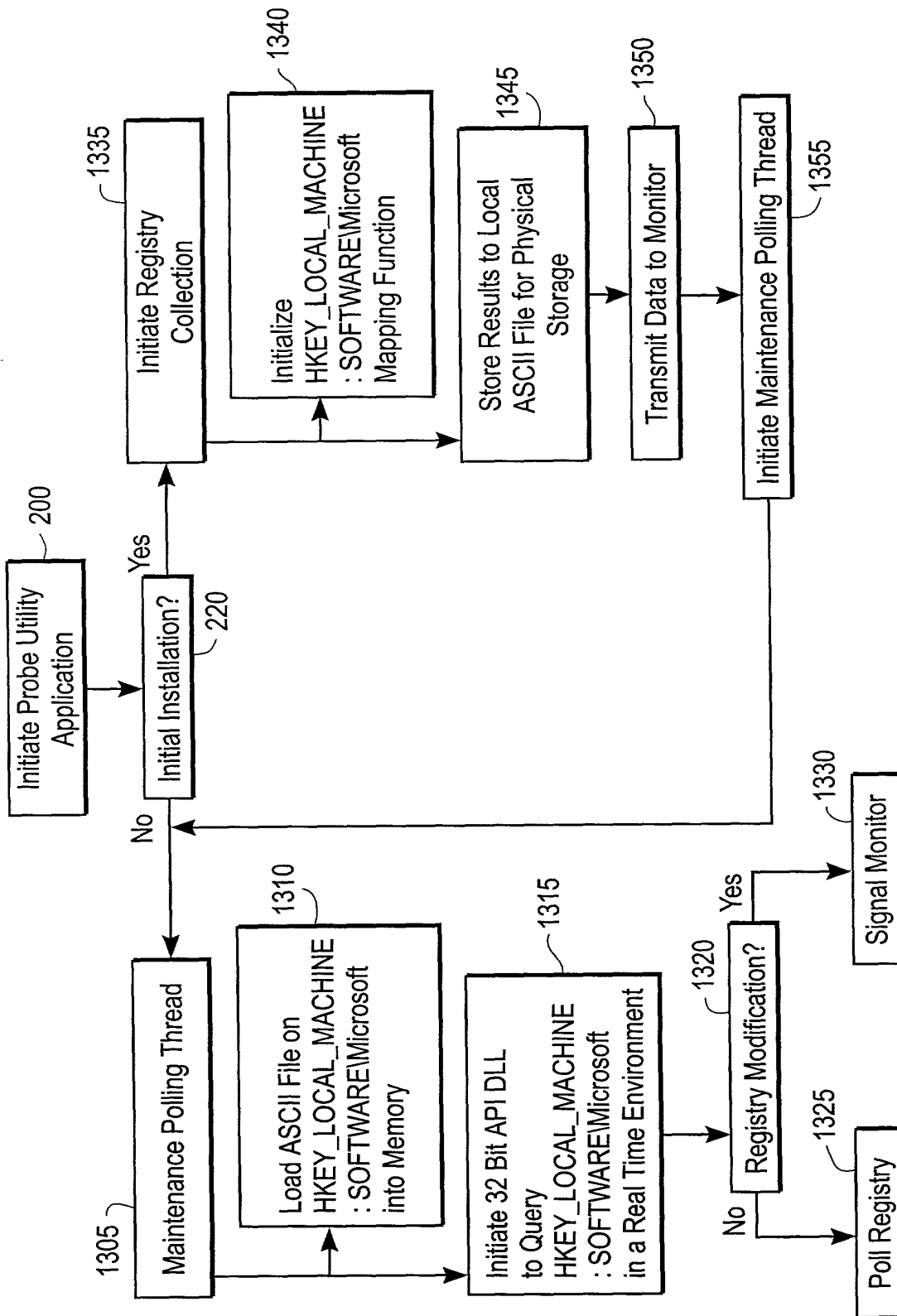


FIG. 13

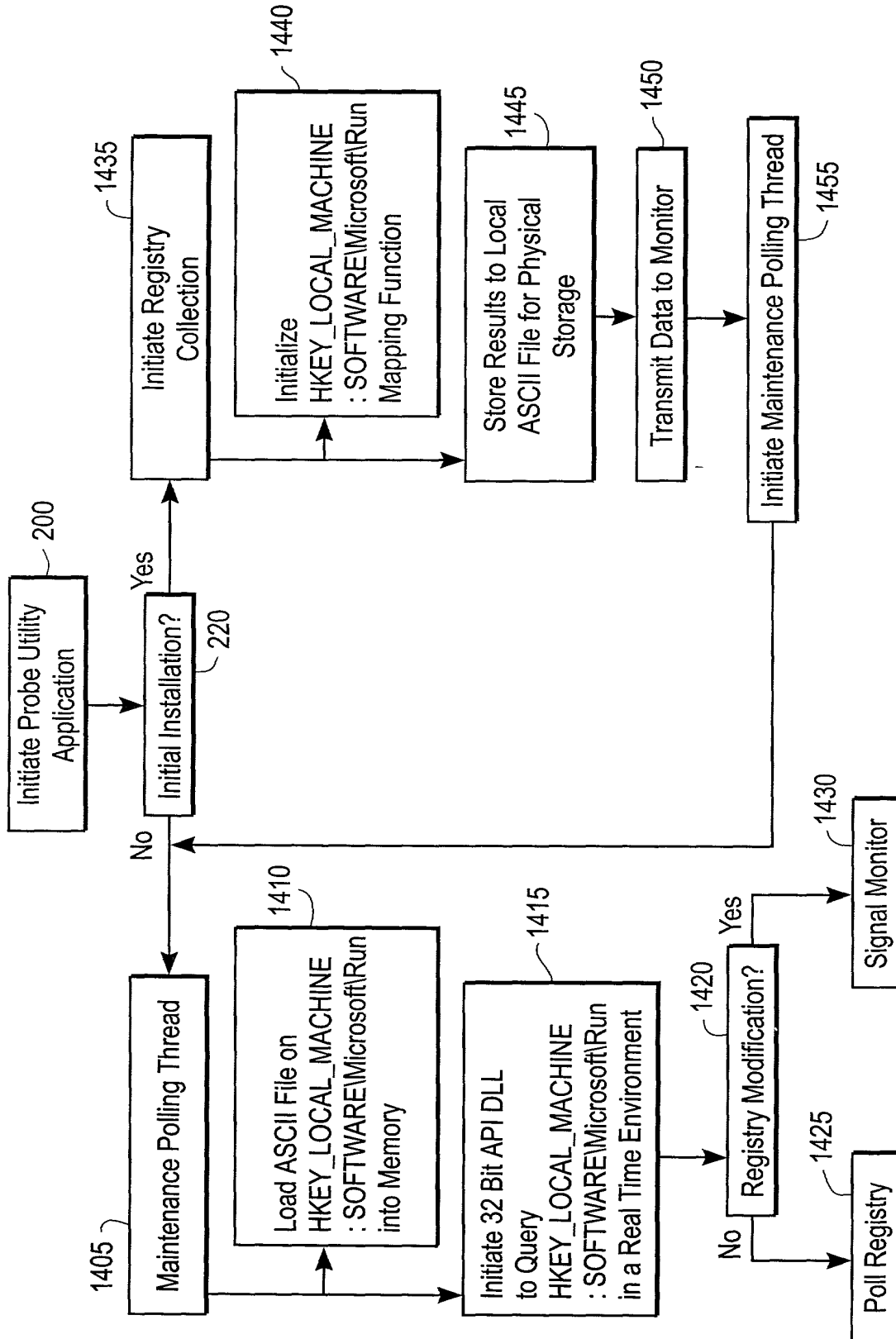


FIG. 14

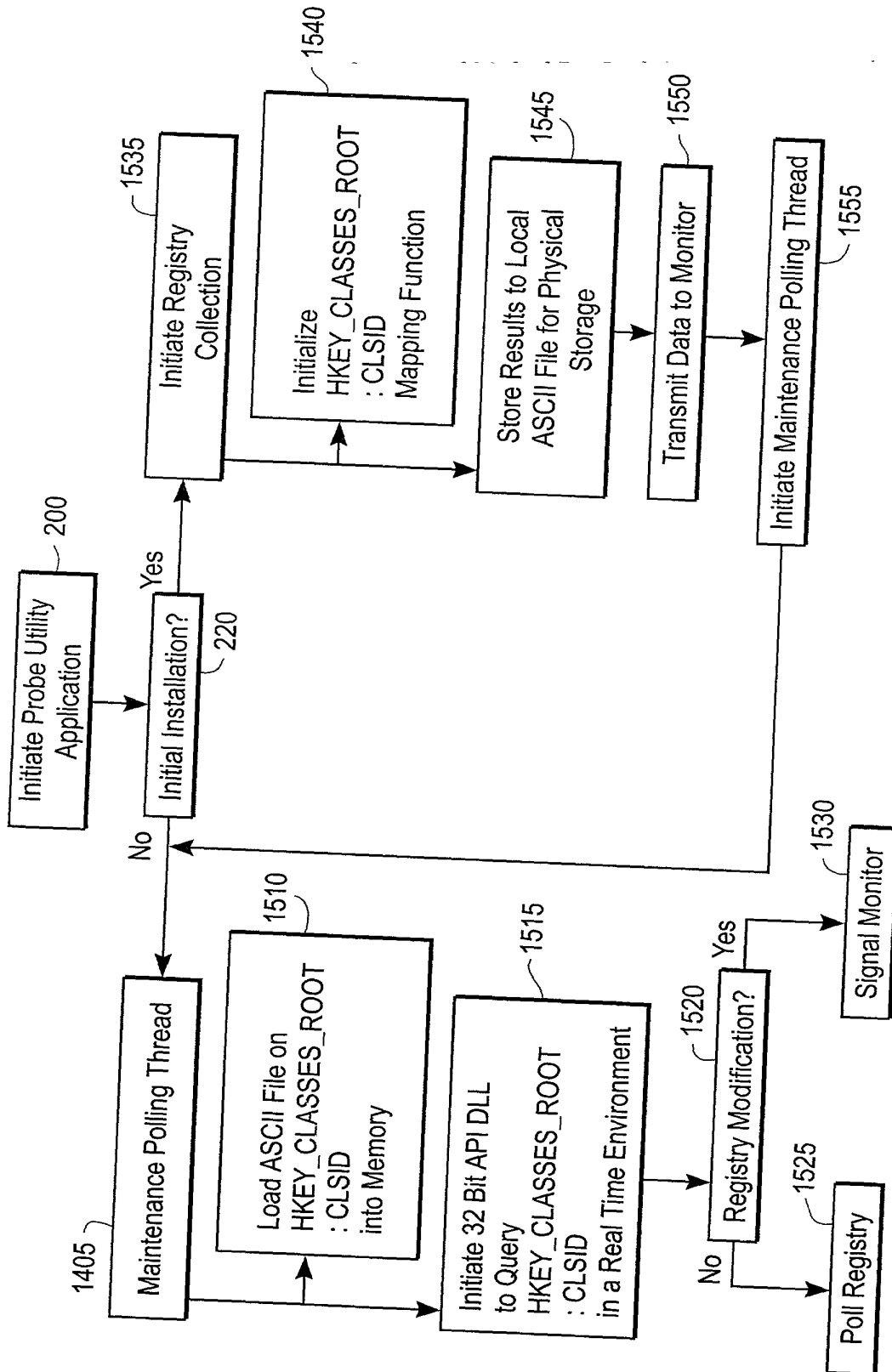


FIG. 15

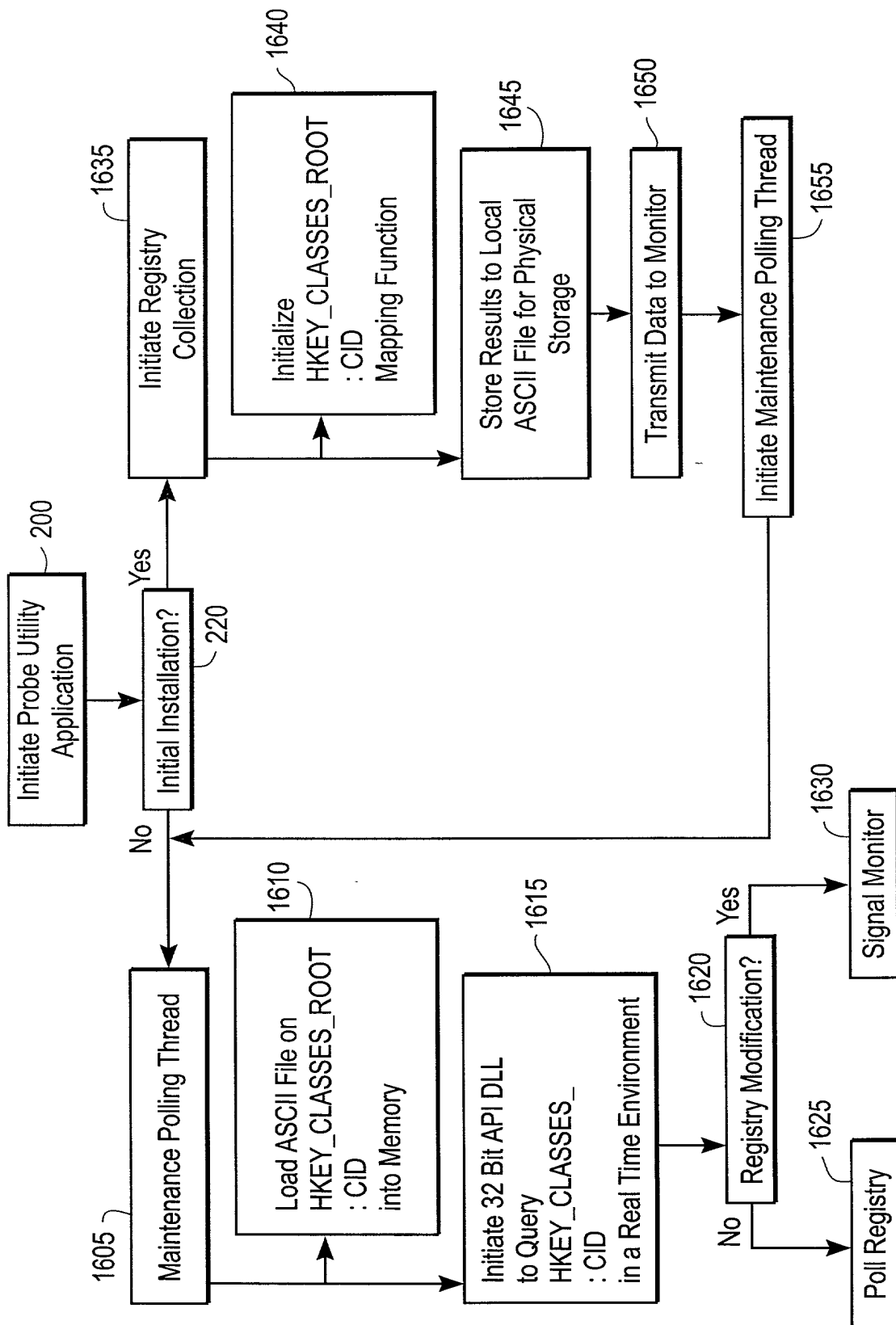


FIG. 16



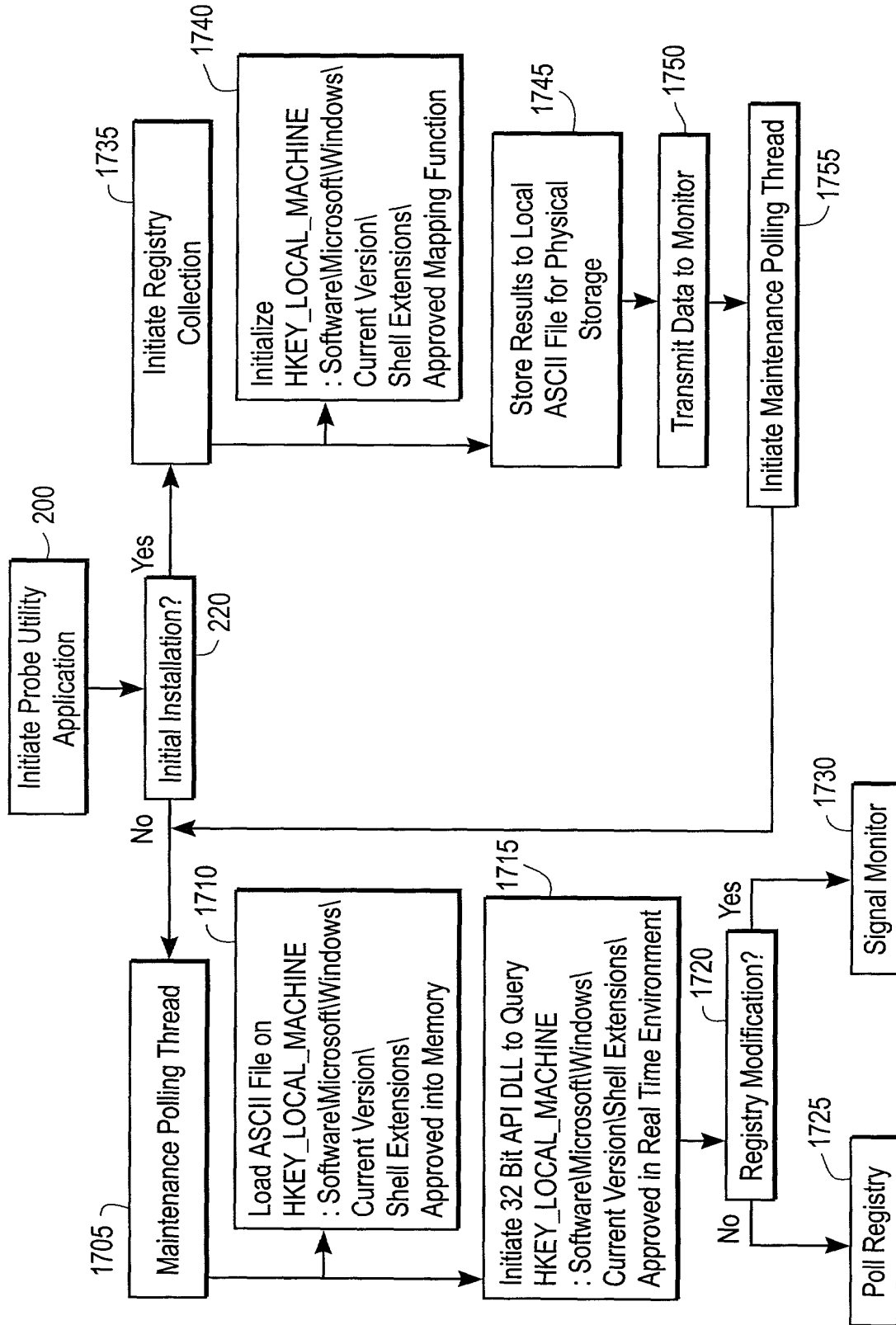


FIG. 17

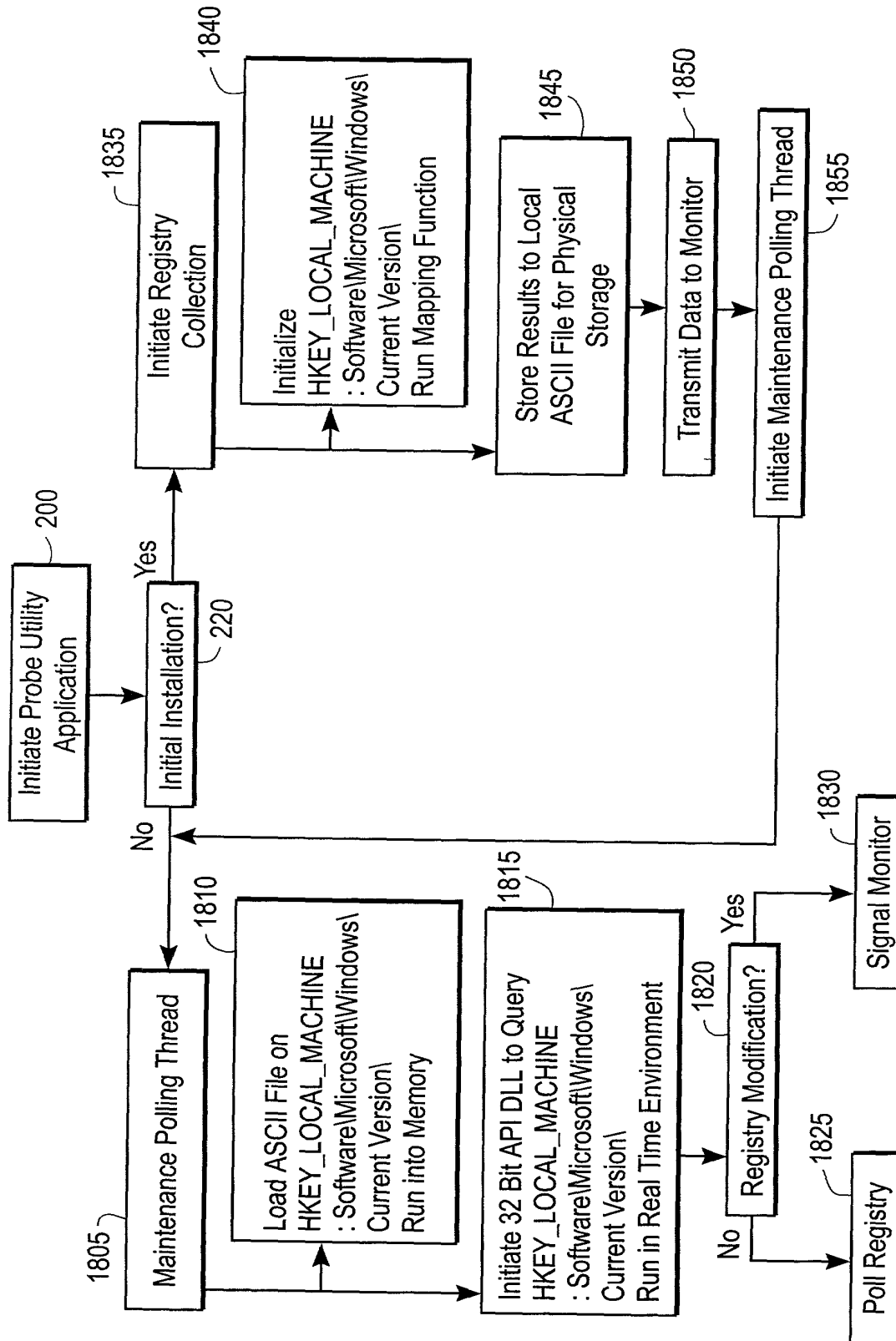


FIG. 18

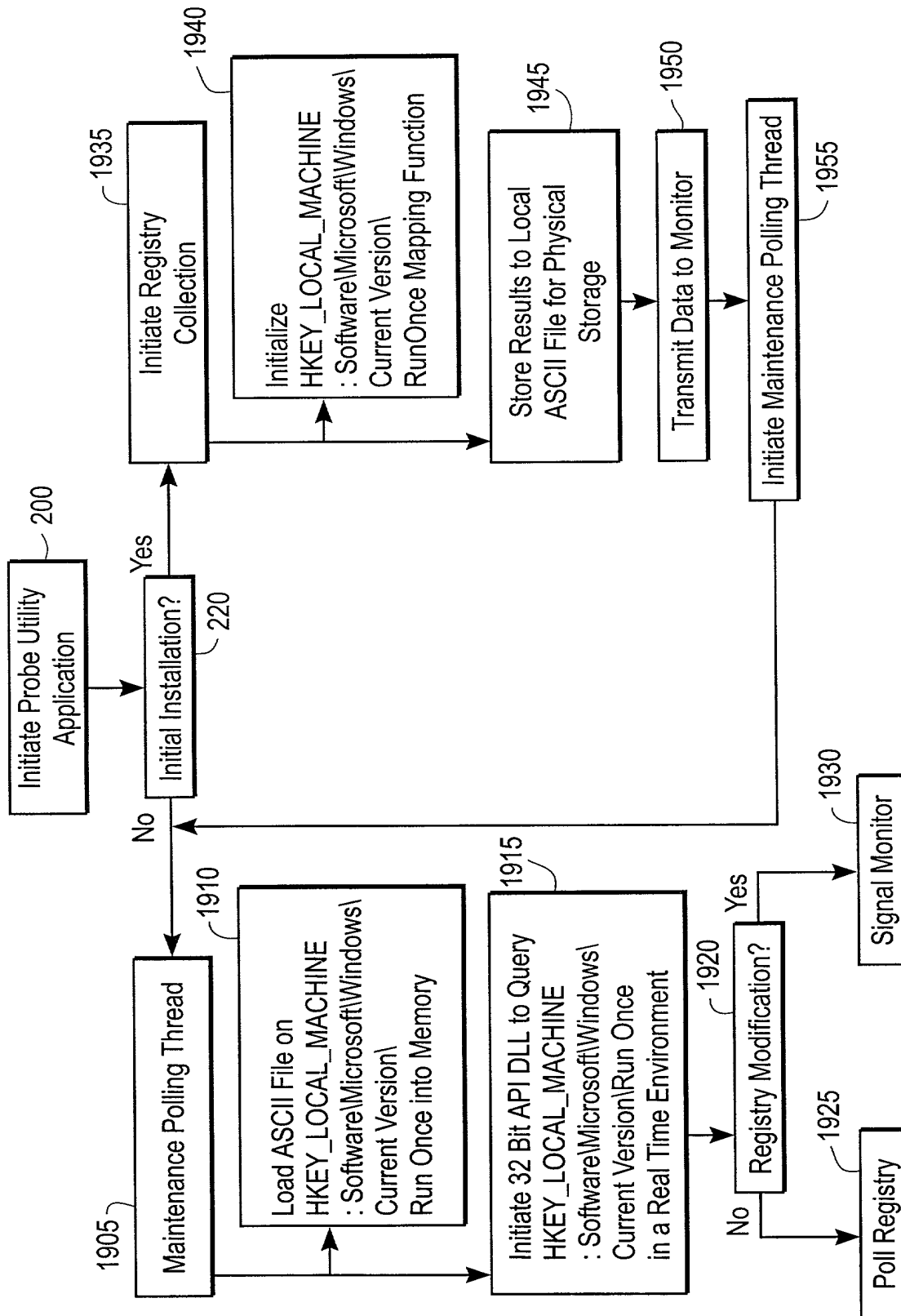


FIG. 19

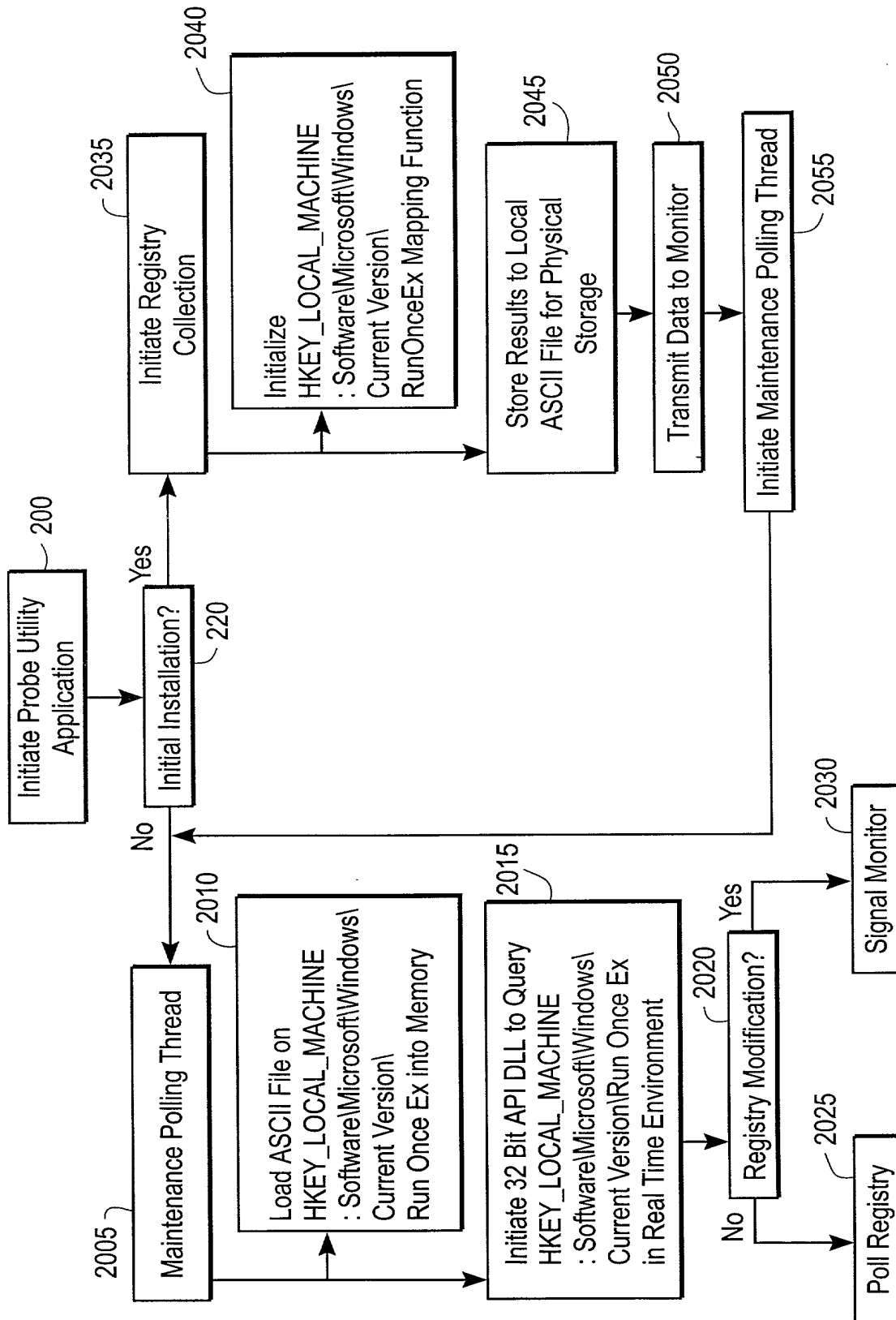


FIG. 20

FIG. 21

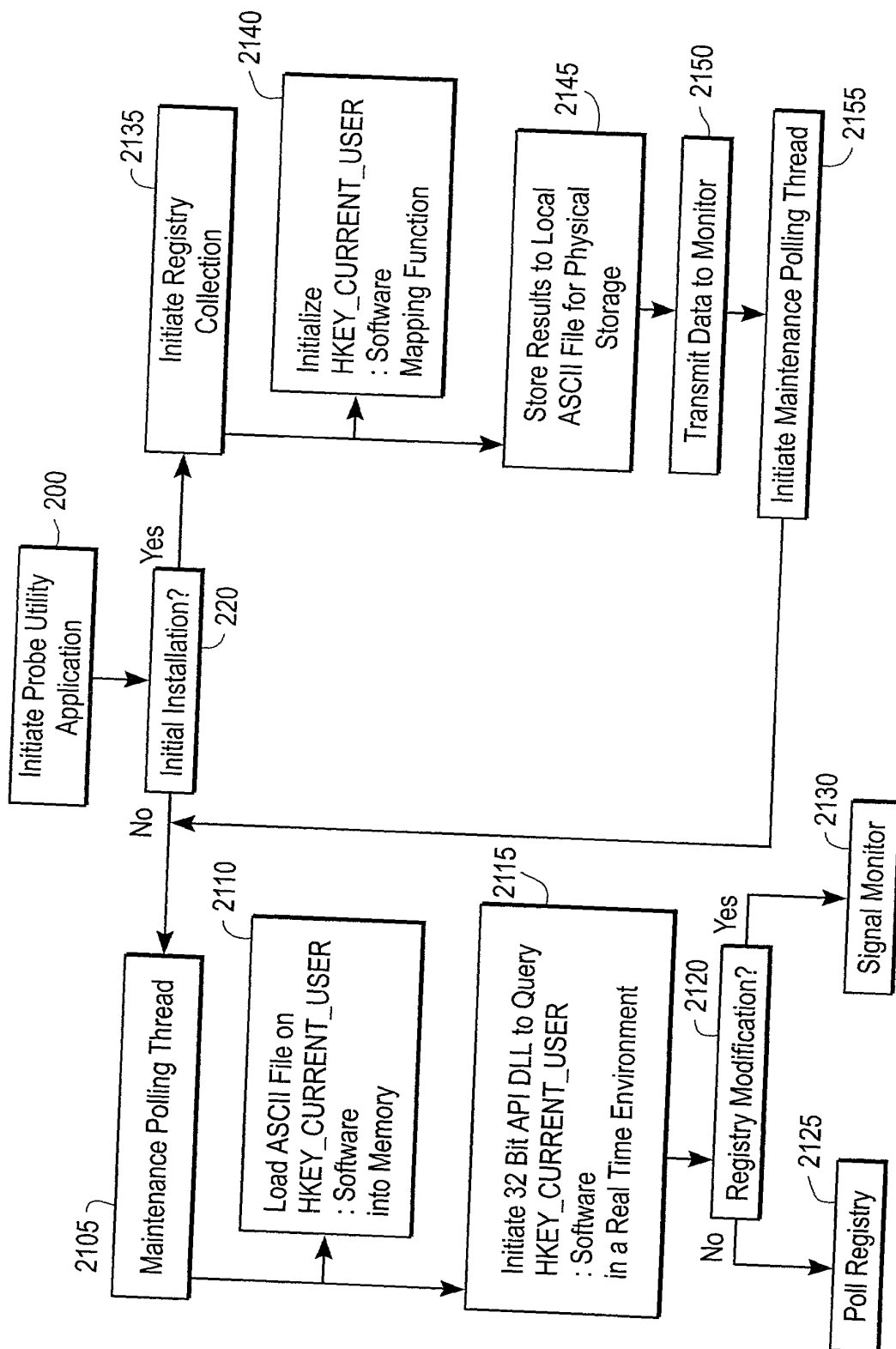
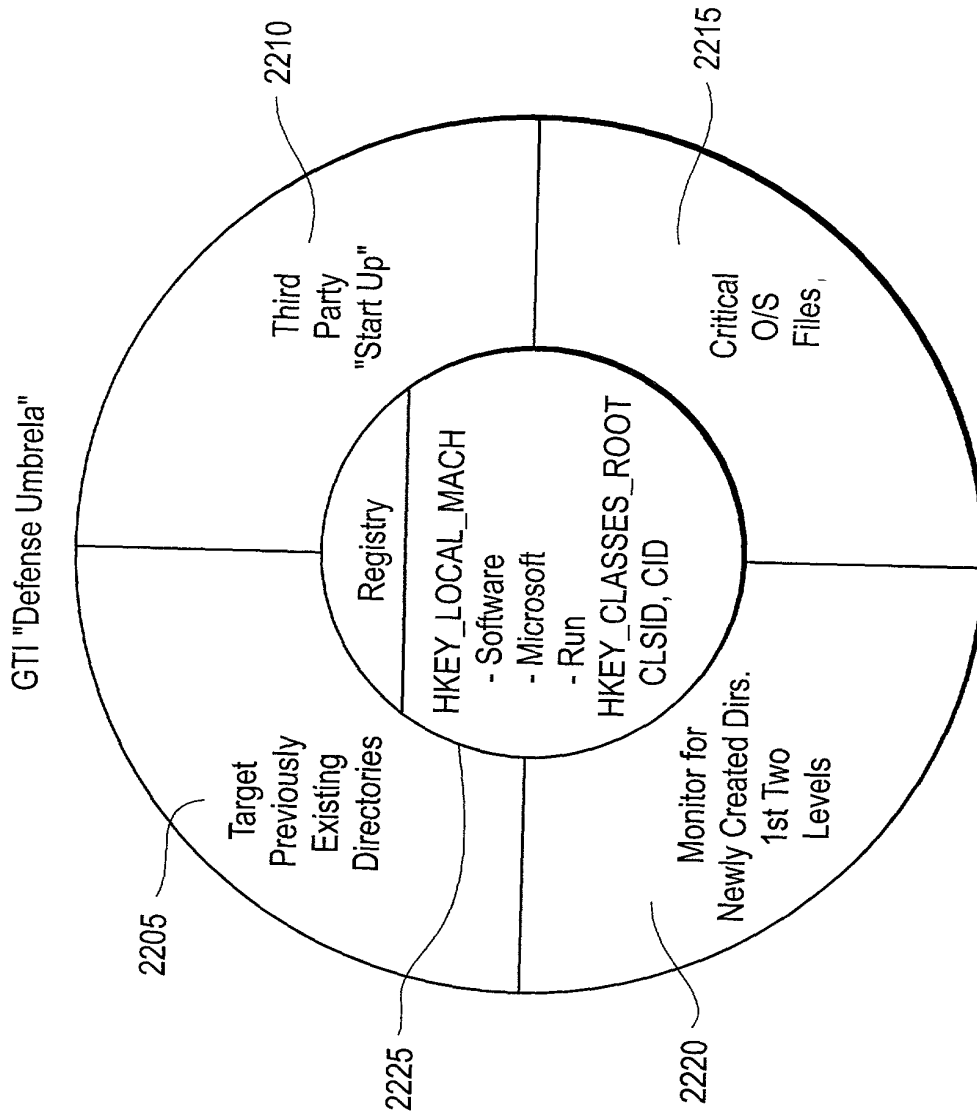


FIG. 21



"Guarded State" of the Complete PC

FIG. 22

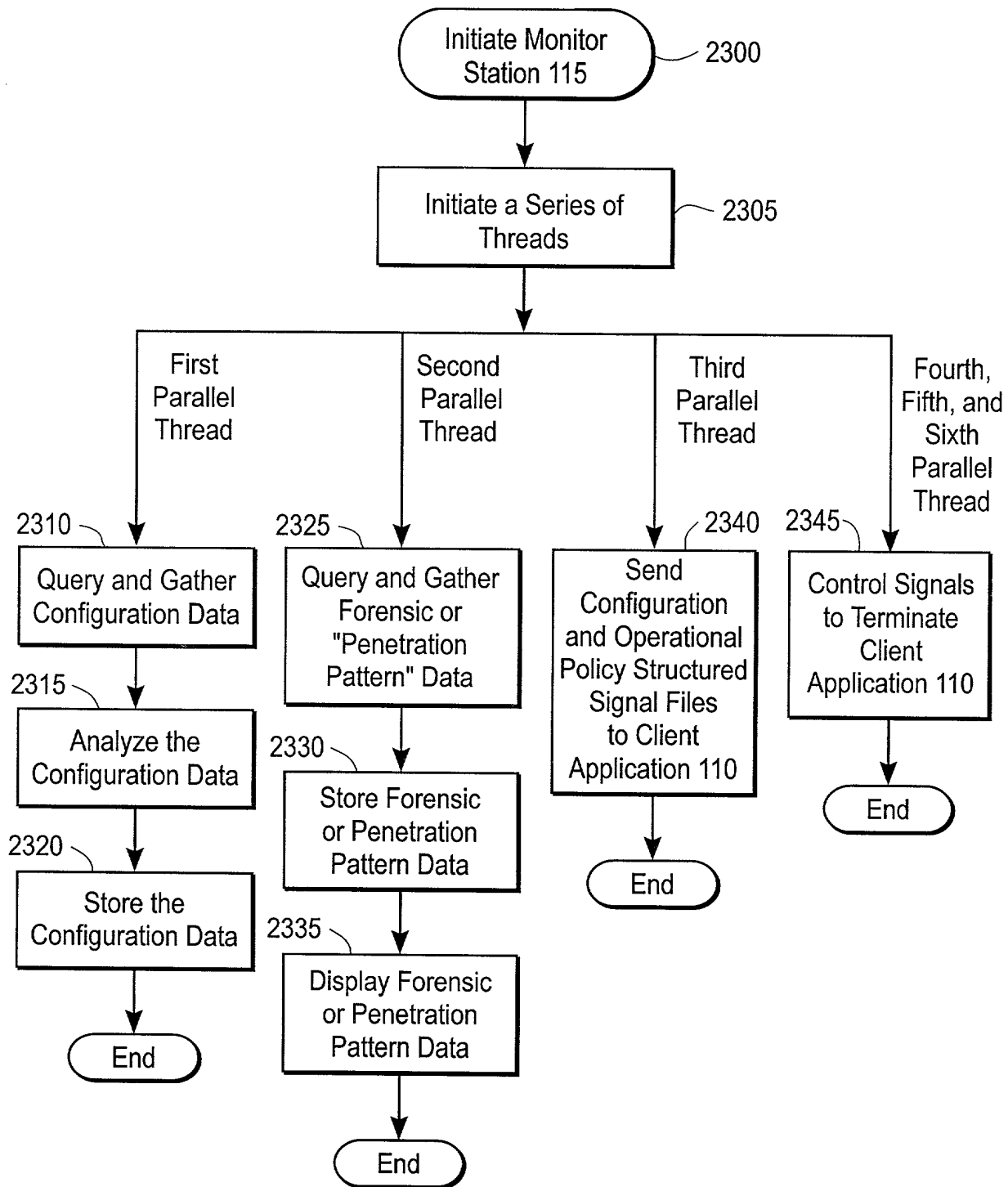


FIG. 23

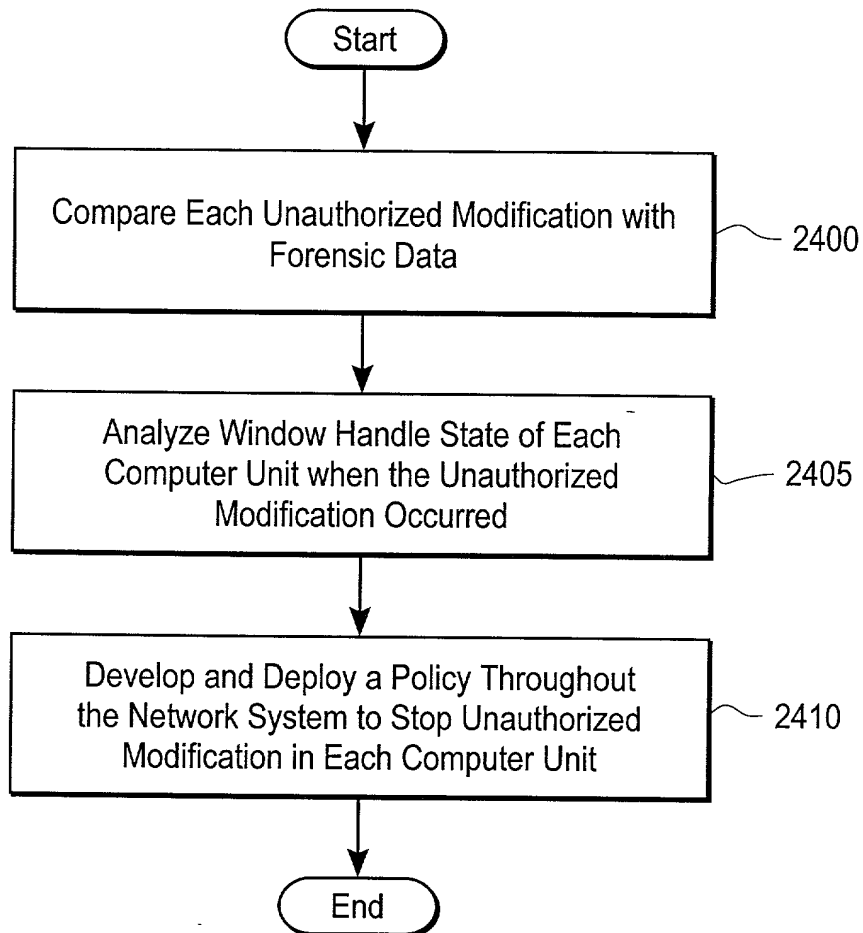


FIG. 24